

b. Replace paragraph [0036] on page 14 with the following text:

[0036]

The display device described above become fully addressable, when: the cathode electrode is configured as array of strip-like cathode electrodes extending substantially in the same direction and to be spaced and electrically insulated from each other at intervals in the transverse direction; the gate electrode is configured as array of strip-like gate electrodes extending in the direction that intersect with the cathode electrodes and to be spaced and electrically insulated from each other at intervals in the transverse direction; one or an array of apertures are formed in the gate electrode and the gate insulator at intersections, each aligned with and exposes one nano-structure in the emitter layer; and the anode electrode is configured as an array of strip-like anode electrodes, each extending opposed to a corresponding gate electrodes. The intersection forms a pixel region corresponding to one pixel of a display.

c. Replace the 3<sup>rd</sup> and 4<sup>th</sup> sentence in paragraph [0068] on page 19, starting on the 5<sup>th</sup> line, with the following two sentences:

[0068]

A patterned catalyst layer 50 of 3 nm thick is then deposited onto the cathode, as is shown in FIG. 3C. The catalyst, Ni for example, can be deposited by thermal or e-beam evaporation, or electrochemical plating through an ion-track-etched membrane laid on top of the substrate.

d. Replace paragraph [0076] on page 25 with the following text:

[0076]